

FIG. 1

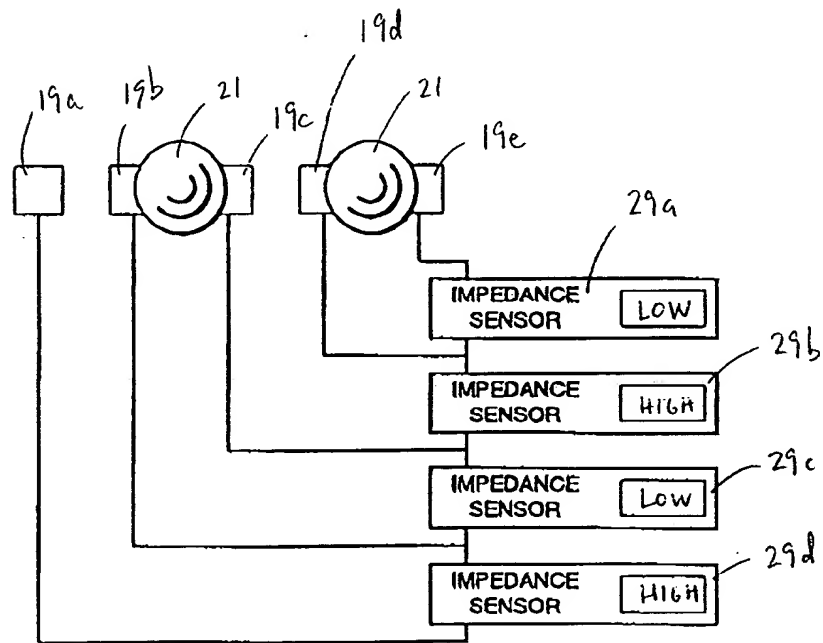


FIG. 3

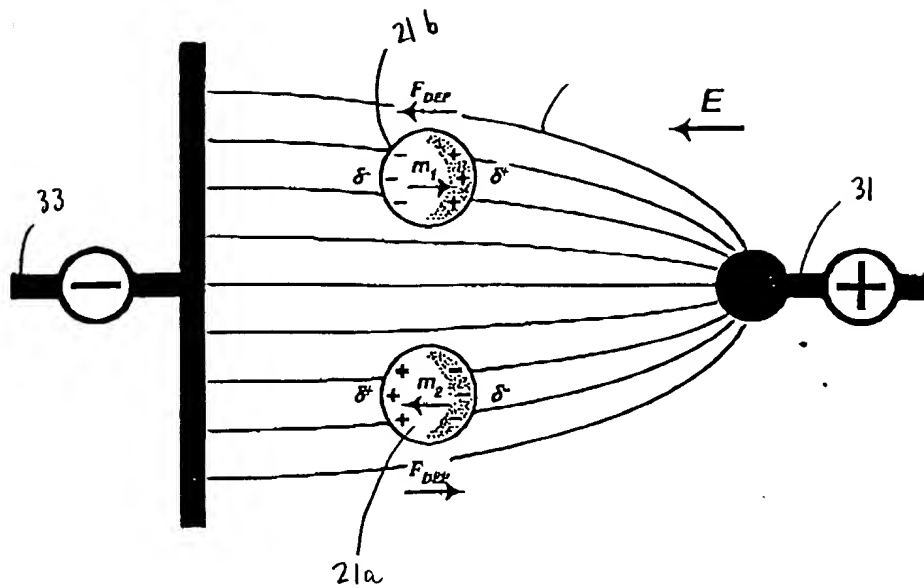


FIG. 2

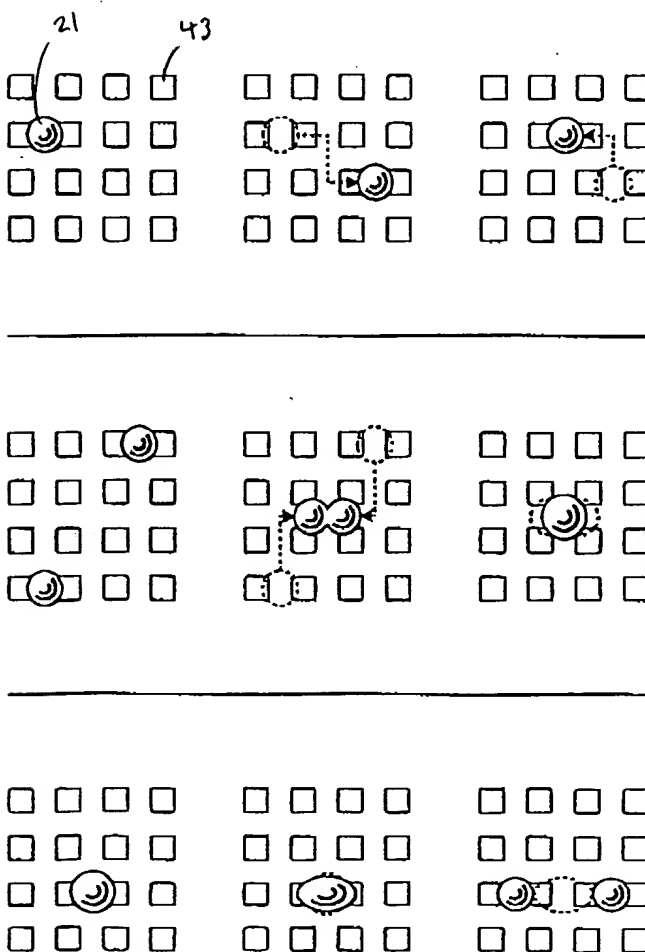


FIG. 12

0000293.071001

0990993-071001

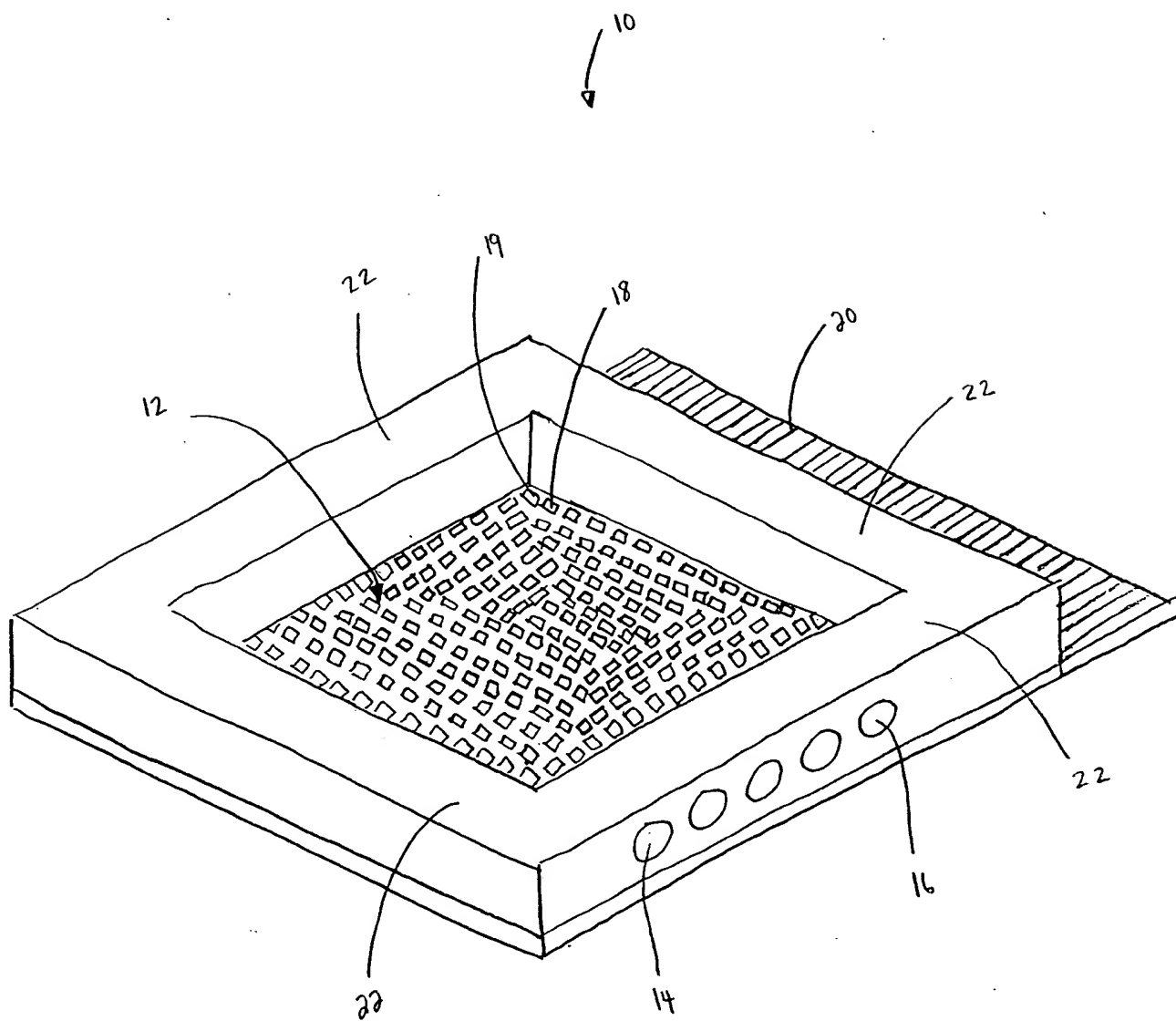


FIG. 4

09902933-071001

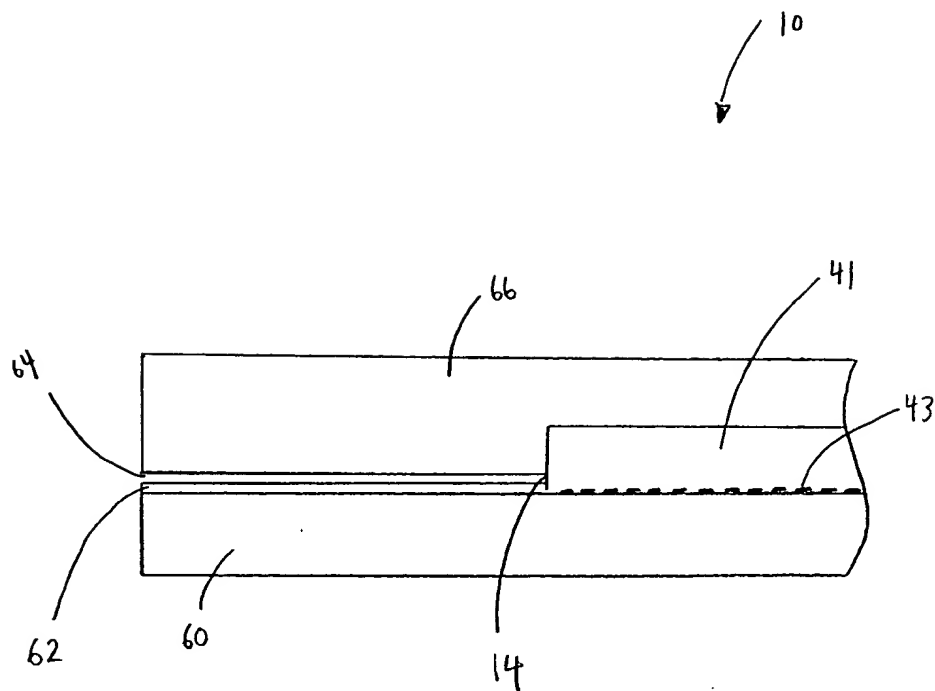


FIG. 5

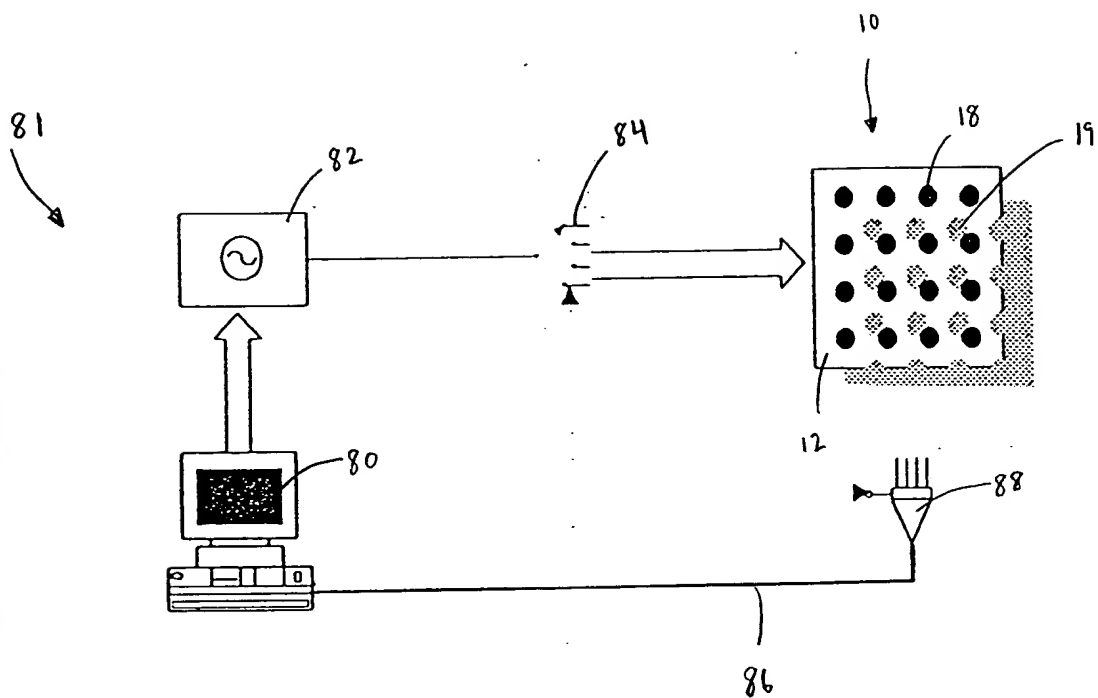


FIG. 6

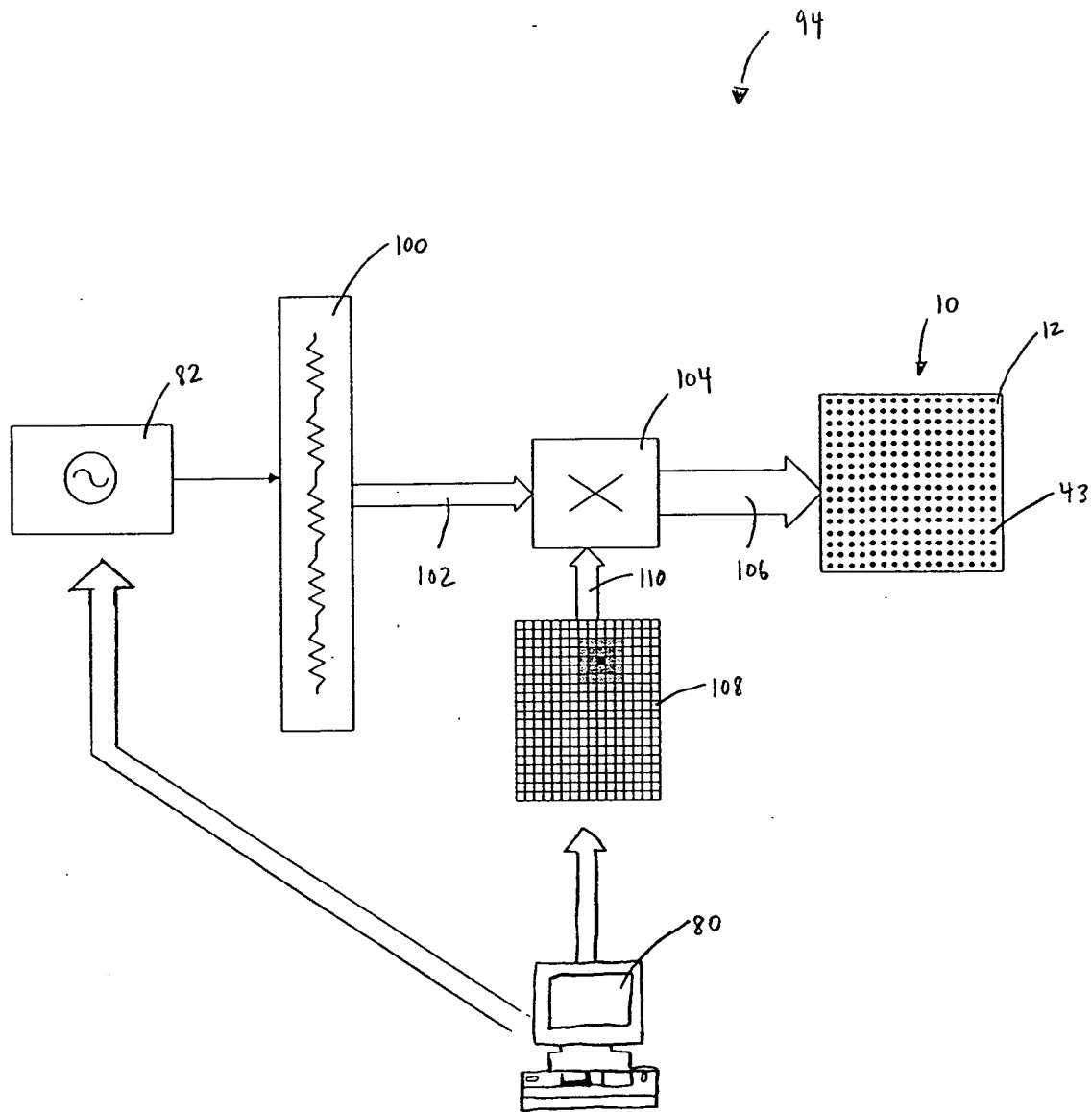


FIG. 7

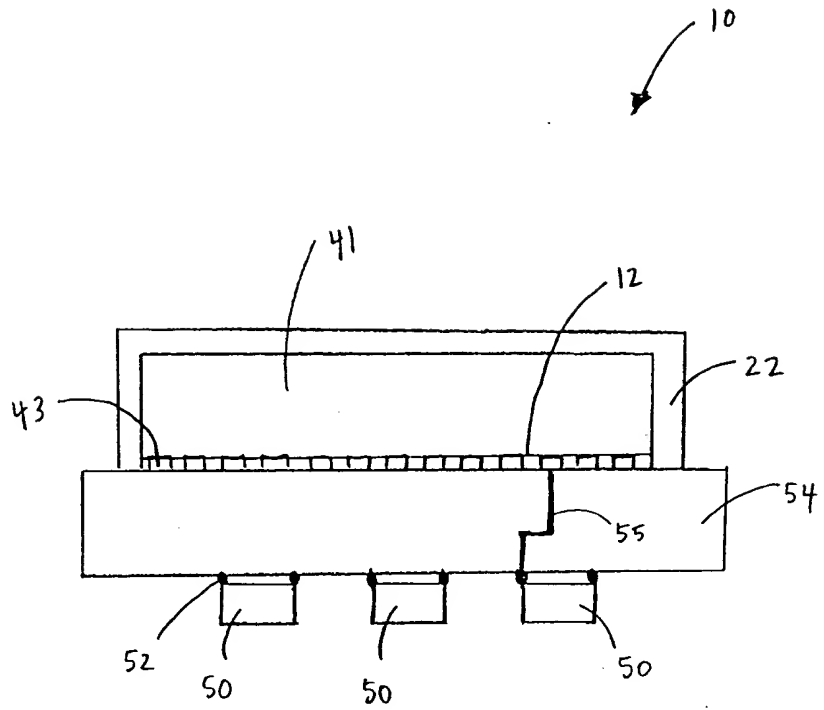


FIG. 8

09902933-071001

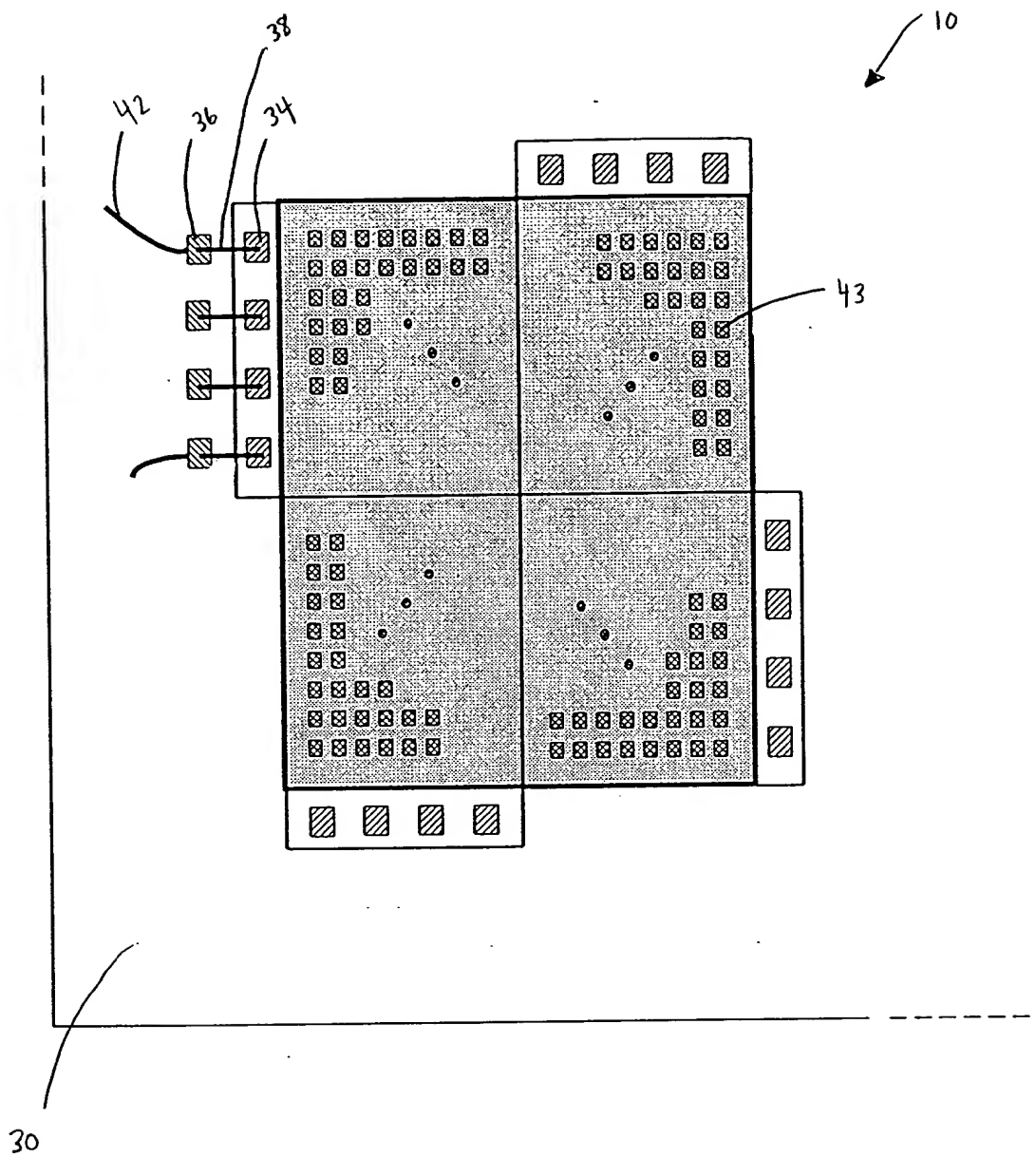


FIG. 9

Multiple programmable inlet/outlet
ports along edges of processor

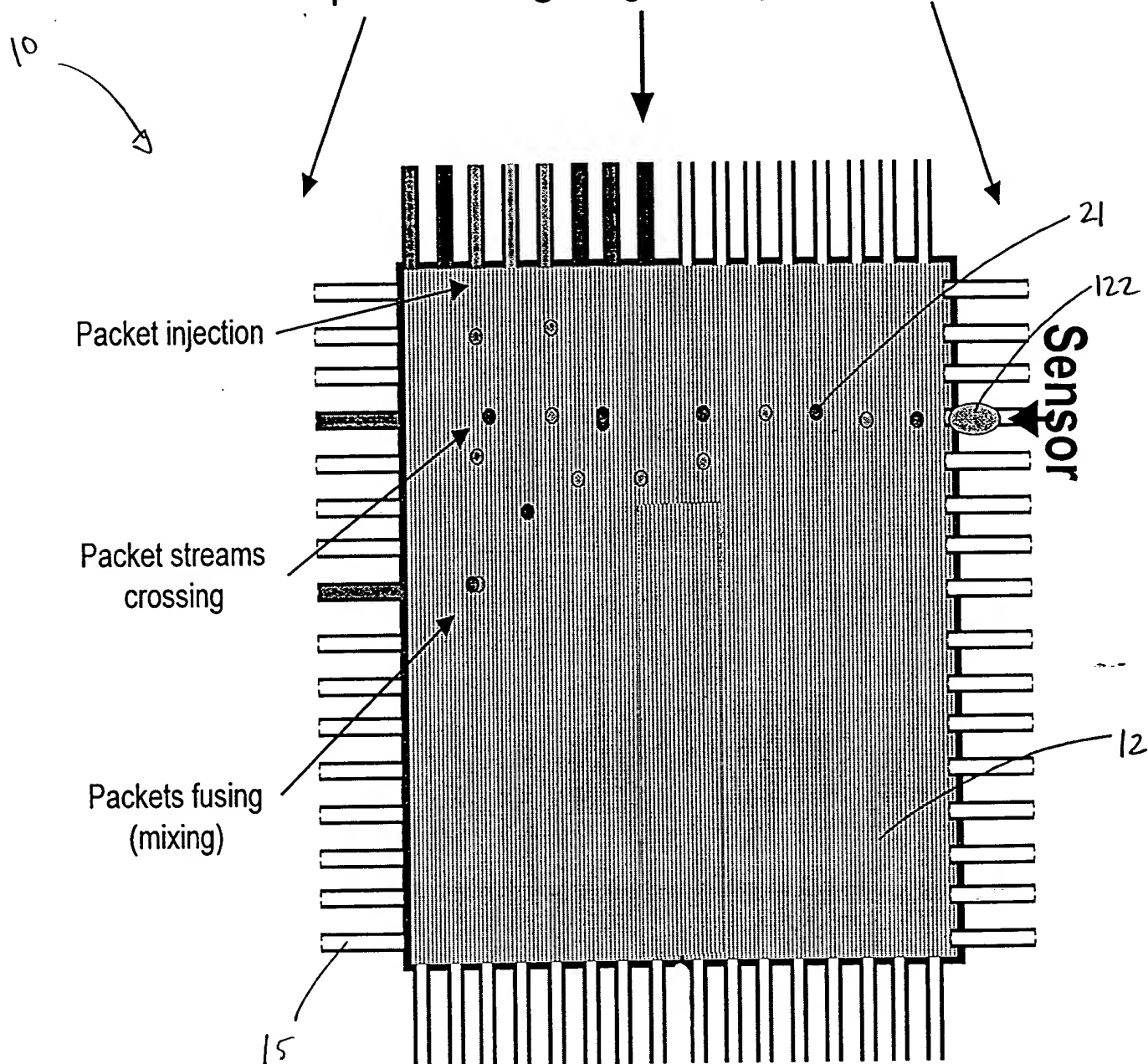


FIG. 9B

FIG. 10

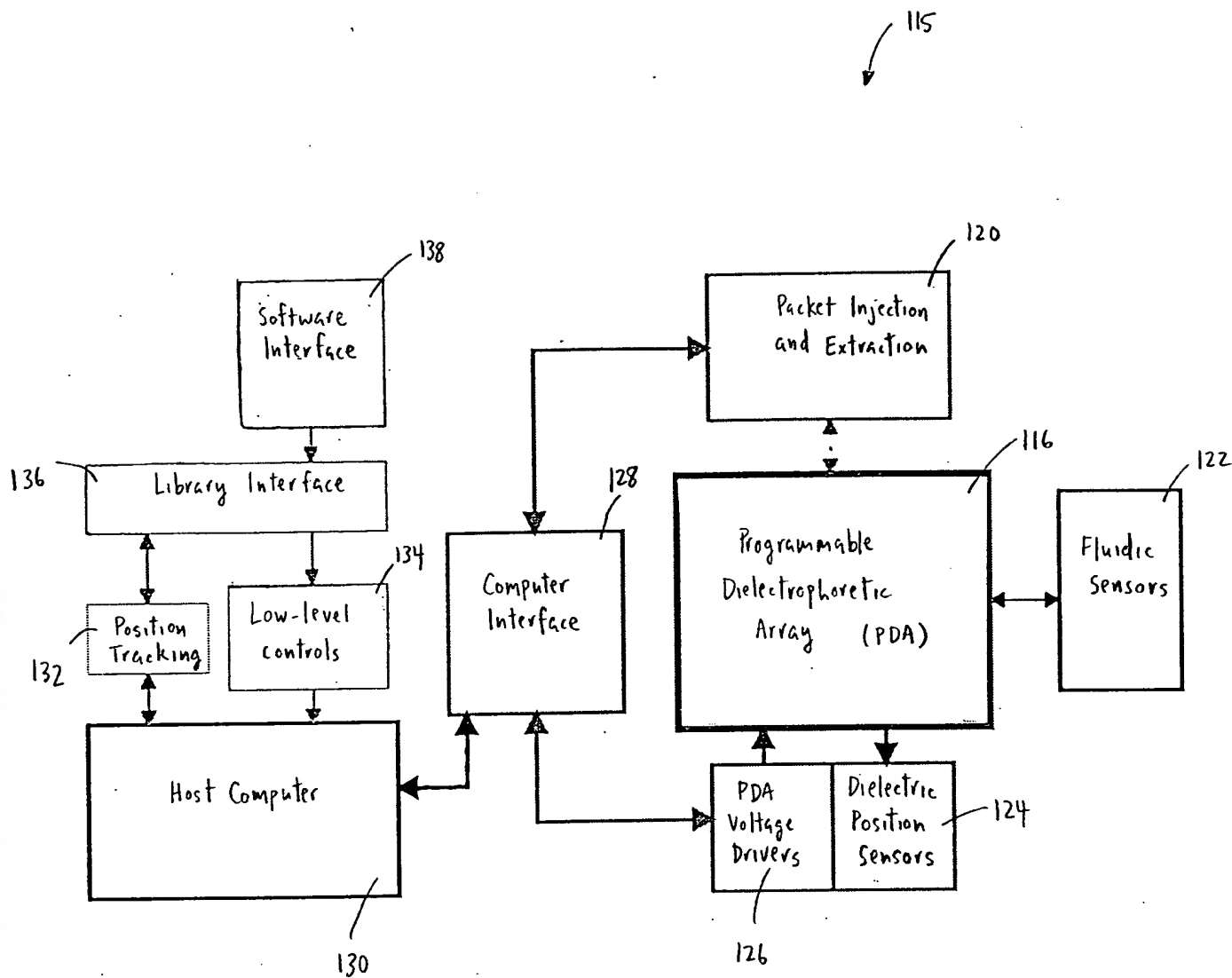


FIG. 10

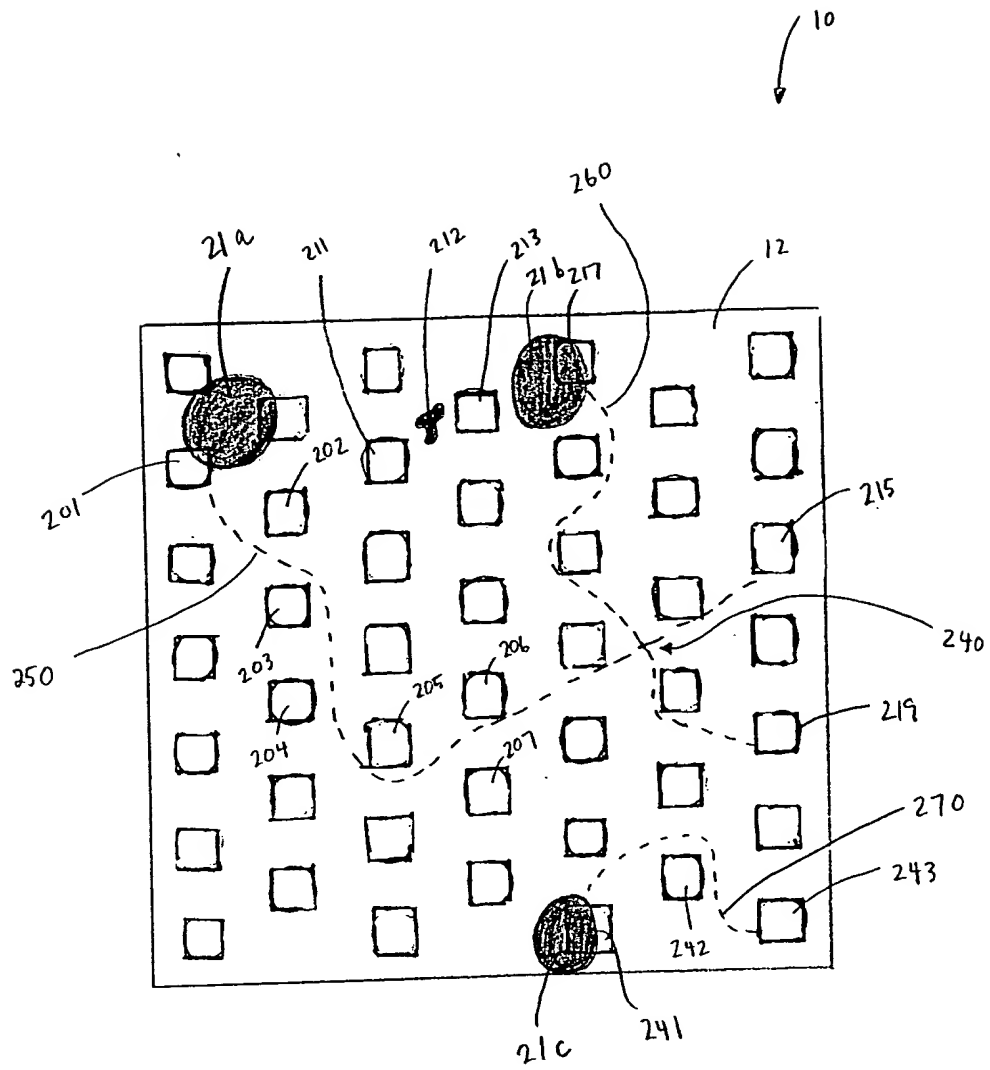


FIG. 11

090293 07104

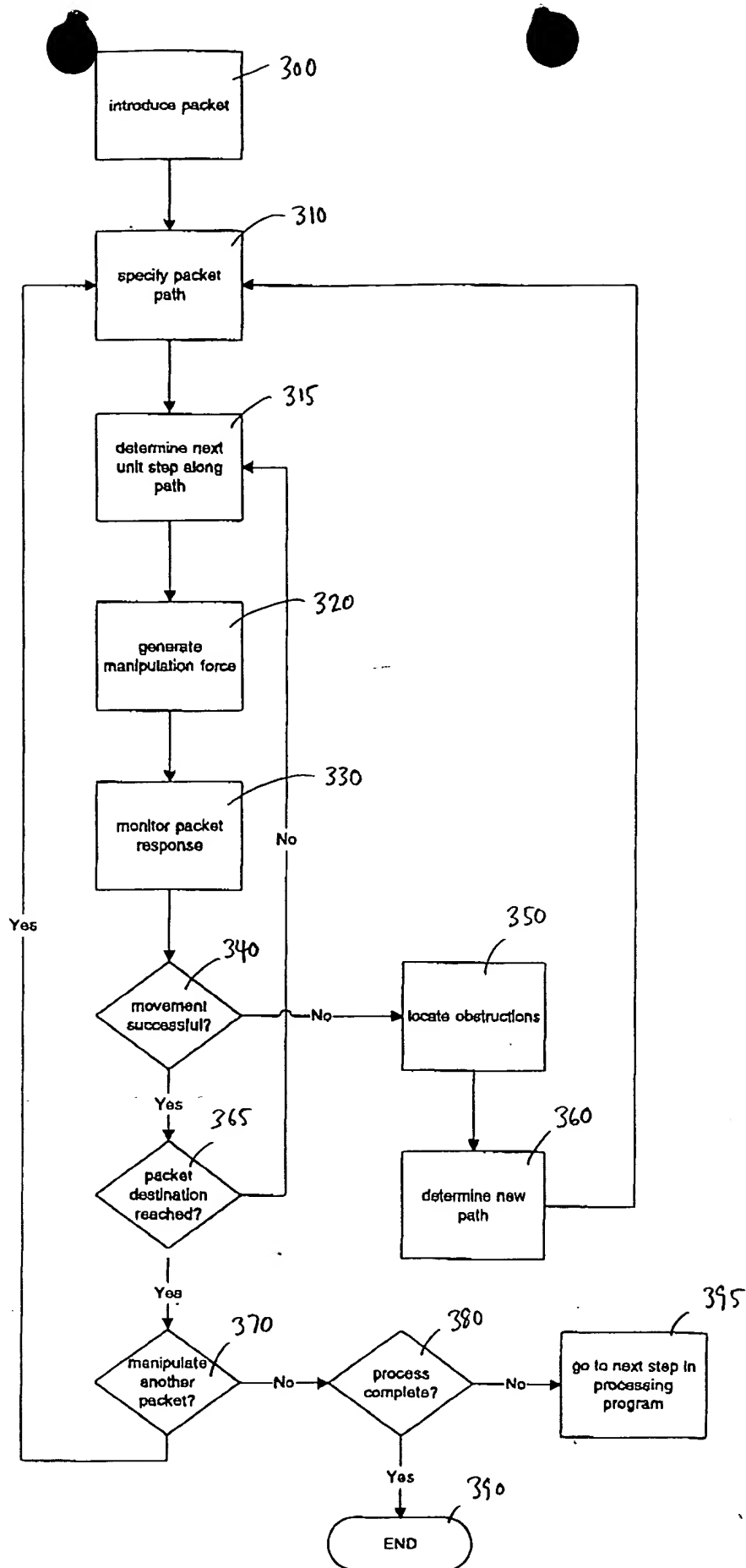


FIG. 13